

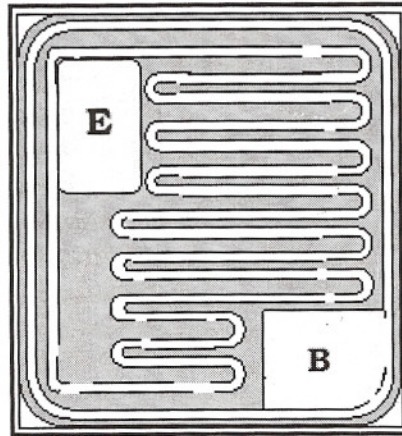


Sierra Components, Inc.

2222 Park Place Building 3 Suite E • Minden, Nevada 89423
Phone: 775.783.4940 Fax: 775.783.4947

Chip back potential is the level which bulk silicon is maintained by on-chip connection, or it is the level to which the chip back must be connected when specifically stated below. If no potential is given the chip back should be isolated.

99 mils



93 mils

Topside Metal: Al
Backside: Tri-metal
Backside Potential: COLLECTOR
Mask Ref:
Bond Pads (Mils): E=18x30, B=20x26

APPROVED BY:
MFG: Fairchild

DIE SIZE (Mils): .099" x .093"
THICKNESS: .007"

DATE: 1/25/05
P/N: D45H8